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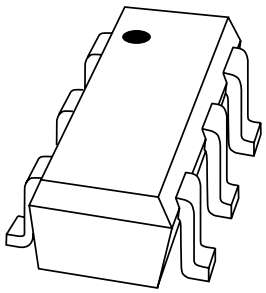
DATA SHEET



PUMH7
NPN resistor-equipped double transistor

Product specification
Supersedes data of 1998 Jun 29

1999 Apr 22

Philips
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NPN resistor-equipped double transistor PUMH7

FEATURES

- Transistors with built-in bias resistor R1 (typ. 4.7 kΩ)
- No mutual interference between the transistors
- Simplification of circuit design
- Reduces number of components and board space.

APPLICATIONS

- Especially suitable for space reduction in interface and driver circuits
- Inverter circuit configurations without use of external resistors.

DESCRIPTION

NPN resistor-equipped double transistor in an SC-88 (SOT363) plastic package.

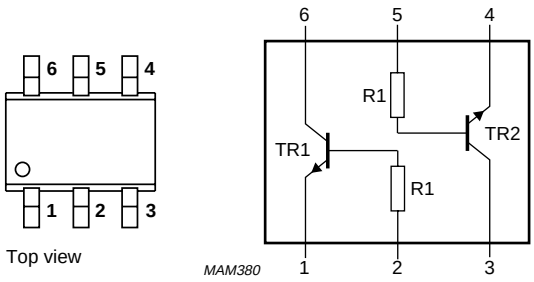


Fig.1 Simplified outline (SC-88; SOT363) and symbol.

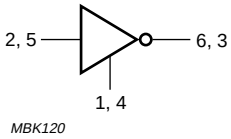


Fig.2 Equivalent inverter symbol.

PINNING

PIN	DESCRIPTION	
1, 4	emitter	TR1; TR2
2, 5	base	TR1; TR2
6, 3	collector	TR1; TR2

MARKING

TYPE NUMBER	MARKING CODE
PUMH7	Ht7

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per transistor					
V_{CBO}	collector-base voltage	open emitter	–	50	V
V_{CEO}	collector-emitter voltage	open base	–	50	V
V_{EBO}	emitter-base voltage	open collector	–	5	V
I_O	output current (DC)		–	100	mA
I_{CM}	peak collector current		–	100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C; note 1	–	200	mW
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	operating ambient temperature		–65	+150	°C
Per device					
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	–	300	mW

Note

1. Refer to SC-88 standard mounting conditions.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-}a}$	thermal resistance from junction to ambient	note 1	416	K/W

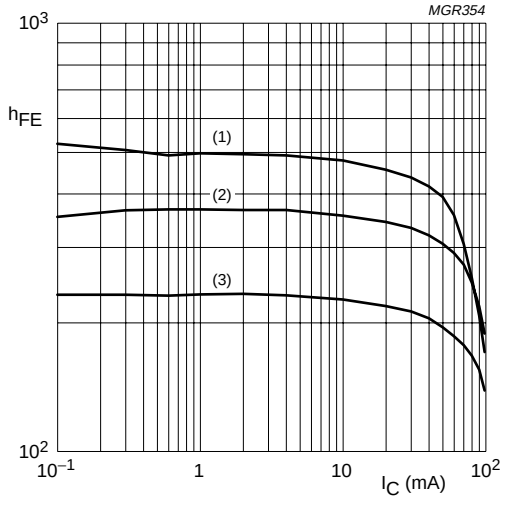
Note

1. Refer to SC-88 standard mounting conditions.

CHARACTERISTICS

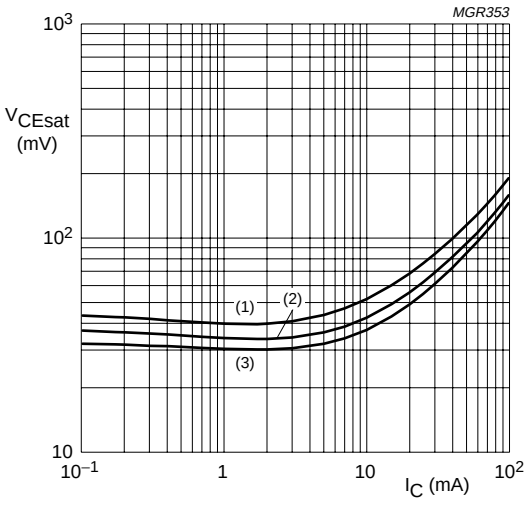
T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Per transistor						
I_{CBO}	collector cut-off current	I_E = 0; V_{CB} = 50 V	–	–	100	nA
I_{CEO}	collector cut-off current	I_B = 0; V_{CE} = 30 V	–	–	1	µA
		I_B = 0; V_{CE} = 30 V; T_j = 150 °C	–	–	50	µA
I_{EBO}	emitter cut-off current	I_C = 0; V_{EB} = 5 V	–	–	100	nA
h_{FE}	DC current gain	I_C = 1 mA; V_{CE} = 5 V	200	330	–	
V_{CEsat}	collector-emitter saturation voltage	I_C = 5 mA; I_B = 0.25 mA	–	–	100	mV
R1	input resistor		3.3	4.7	6.1	kΩ
C_c	collector capacitance	$I_E = i_e$ = 0; V_{CB} = 10 V; f = 1 MHz	–	–	2.5	pF



V_{CE} = 5 V.
(1) T_{amb} = 100 °C.
(2) T_{amb} = 25 °C.
(3) T_{amb} = –40 °C.

Fig.3 DC current gain as a function of collector current; typical values; per transistor.

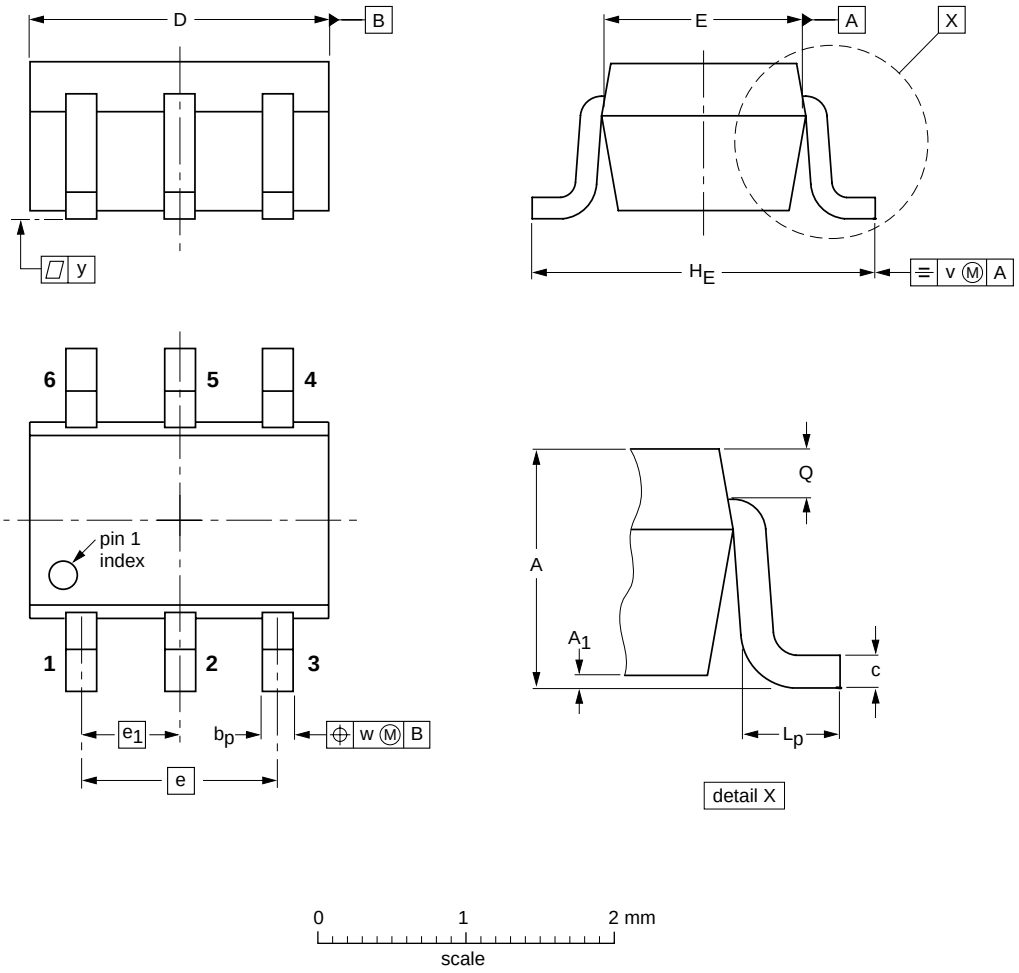


I_C/I_B = 20.
(1) T_{amb} = 100 °C.
(2) T_{amb} = 25 °C.
(3) T_{amb} = –40 °C.

Fig.4 Collector-emitter saturation voltage as a function of collector current; typical values; per transistor.

PACKAGE OUTLINE

Plastic surface mounted package; 6 leads **SOT363**



DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1 max	b_p	c	D	E	e	e_1	H_E	L_p	Q	v	w	y
mm	1.1 0.8	0.1	0.30 0.20	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.25 0.15	0.2	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT363			SC-88			97-02-28

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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NOTES

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